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- IEEE與IEEE Xplore簡介
- 高效檢索科研論文
- 搜索追蹤學術熱點
- 文獻綜述淺談



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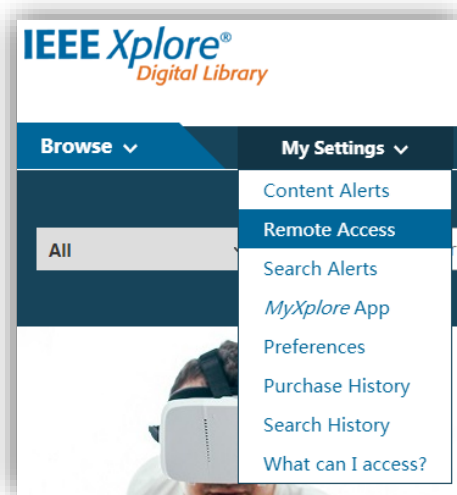
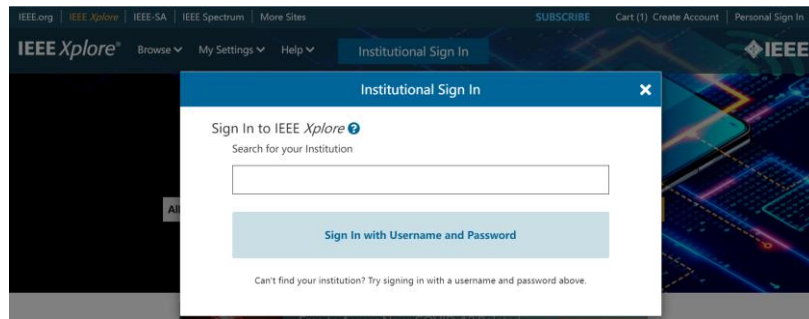
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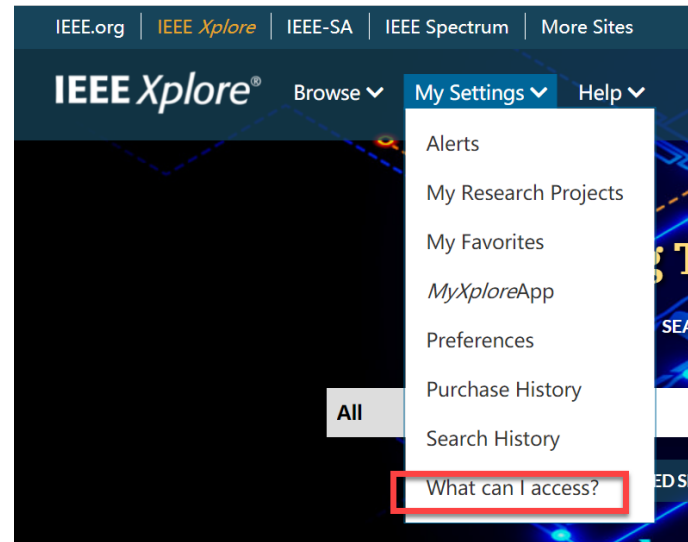
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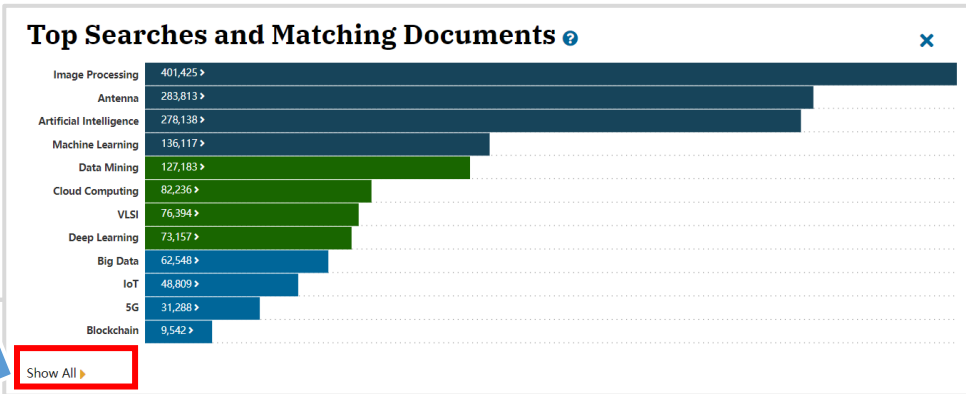
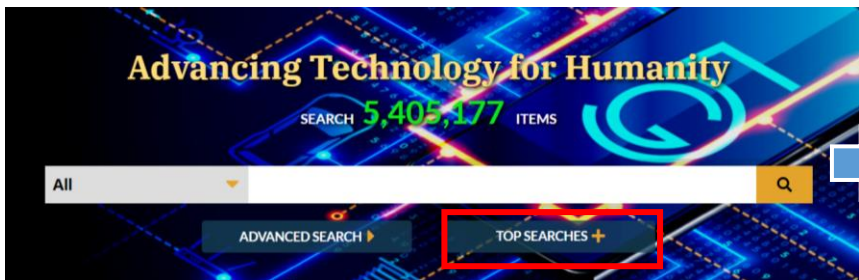


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The Chinese University of Hong Kong
Hong Kong, China

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Biography

Raymond W. Yeung (Fellow, IEEE) was born in Hong Kong in June 1962. He received the B.S., M.Eng., and Ph.D. degrees in electrical engineering from Cornell University, Ithaca, NY, USA, in 1984, 1985, and 1988, respectively. He was on leave at the École Nationale Supérieure des Télécommunications, Paris, France, in Fall 1986. He was a Member of Technical Staff of AT&T Bell Laboratories from 1988 to 1991. Since 1991, he has been with The Chinese University of Hong Kong, where he is currently a Choh-Ming Li Professor of information engineering and the Co-Director of the Institute of Network Coding. He has held visiting positions at Cornell University, Nankai University, Bielefeld University, the University of Copenhagen, the Tokyo Institute of ... [Show More](#)

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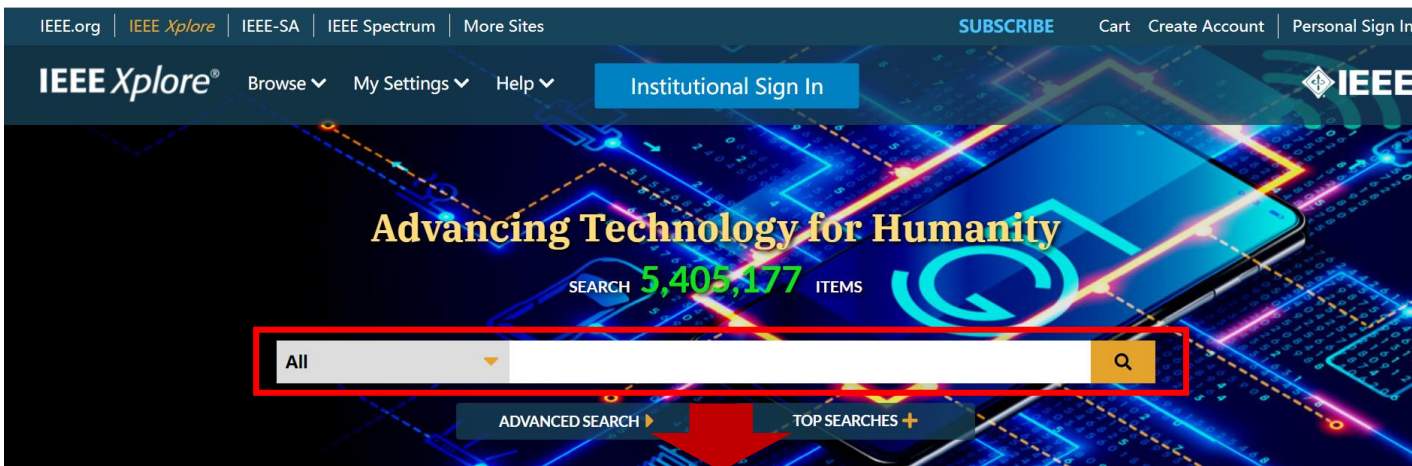
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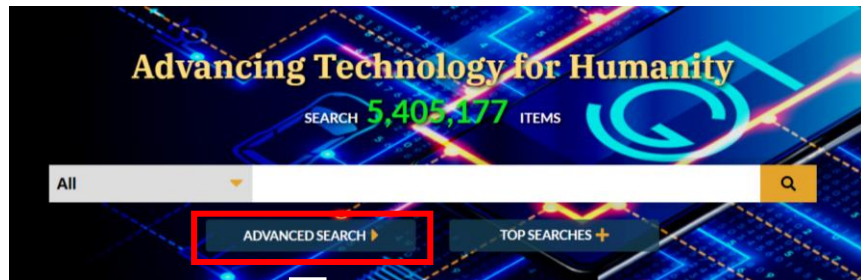
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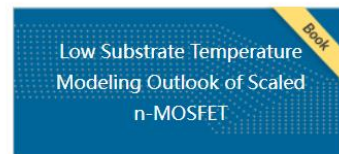
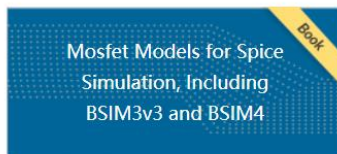
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- Forward-voltage-tunable schottky-integrated trench MOSFETs**
Chiao-Shun Patrick Chuang; Kai-Yu Gary Chen; Yu-Ren Ryan Hung; Ta-Chuan Kuo; Cheng-Chin Tony Huang
2014 IEEE 26th International Symposium on Power Semiconductor Devices & IC's (ISPSD)
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Layout techniques for MOSFETs
Salvador Pinillos Gimenez
Year: 2016 | Book | Publisher: Morgan & Claypool
▶ Abstract



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Enhanced Electrical and Thermal Properties of Trench Metal-Oxide Semiconductor Field-Effect Transistor Built on Copper Substrate

Qi Wang; Ihsiu Ho; Minhua Li

IEEE Electron Device Letters, Vol. 30, No. 1, January 2009

Year: 2009 | Volume: 30 | Issue: 1 | Journal Article | Publisher: IEEE

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Fabrication and Characterization of a Metal Oxide Semiconductor Field Effect Transistor (MOSFET)-based Micro pH Sensor

文章細節頁面

Hybrid MOSFET/driver for ultra-fast switching

T. Tang; C. Burkhart

IEEE Transactions on Dielectrics and Electrical Insulation

Year: 2009 | Volume: 16, Issue: 4 | Journal Article | Publisher: IEEE

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Abstract:

The ultra-fast switching of power MOSFETs, in about 1 ns, is very challenging due to the parasitic inductance that is intrinsic to commercial packages used for power MOSFET drivers. Parasitic gate and source inductance not only limit the voltage rise time of the internal gate structure but can also cause the gate voltage to oscillate. This paper describes a hybrid approach that substantially reduces the parasitic inductance between the driver and MOSFET gate, as well as between the MOSFET source and its external connection. A flip-chip assembly is used to directly attach a die-form power MOSFET and driver on a PCB. The parasitic inductances are significantly reduced by eliminating bond wires and minimizing lead length. The experimental results demonstrate ultra-fast switching of the power MOSFET with excellent control of the gate-source voltage.

Published in: IEEE Transactions on Dielectrics and Electrical Insulation (Volume: 16, Issue: 4, August 2009)

Page(s): 967 - 970

INSPEC Accession Number: 10847239

9.5211841

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Coreless printed circuit board (PCB) transformers for power MOSFET/IGBT gate drive circuits
IEEE Transactions on Power Electronics
Published: 1999

The reliability of high-lead solder joints in flip-chip devices
2014 15th International Conference on Electronic Packaging Technology
Published: 2014

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dv/dt Immunization limit of LV MOSFET in cascode GaN FET and dv/dt safe chart for MOSFETs

Publisher: IEEE

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Z. Chen ; Jaume Roig Guitart **All Authors**

5
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References

Citation Map

1. R. Mitova and R. Ghosh, "Investigations of 600V GaN HEMT and GaN diode for the power converter applications", *IEEE Trans. Power Electron.*, vol. 29, no. 5, pp. 2441-2452, May 2014.
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2. "Application Advantages and Disadvantages of Modern Fast Switching MOSFETs in VRM", *PCIM Europe*, 2016.
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3. Christian R. Müller and Stefan Buschhorn, "Impact of module parasitics on the performance of fast switching devices", *PCIM Europe*, 2014.
▶ Show in Context Google Scholar
4. Alan Elbanhawy, MOSFET Susceptibility to Cross Conduction, Power Electronics Technology, April 2005.
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5. Alan Elbanhawy, AN-7019 Limiting Cross-Conduction Current in Synchronous Power Converter Designs, Fairchild Semiconductor.
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Requirements for low intermodulation distortion in GaN-Al/sub x/Ga/sub 1-x/N high electron mobility transistors: a model assessment
IEEE Transactions on
Published: 2002

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1. Masahiro Koyama, Kentaro Ikeda, Kazuto Takao, "Novel cascode GaN module integrated a single gate driver IC with high switching speed controllability", *Power Electronics and Applications (EPE'18 ECCE Europe) 2018 20th European Conference on*, pp. P.1-P.8, 2018.
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2. Tianhua Zhu, Fang Zhuo, Feng Wang, Hailin Wang, Xiaoping Sun, Shuhuai Shi, Baohui Ma, "Quantitative Analysis and Suppression Strategies of Dv/dt Induced Turn-on of Cascode GaN FETs in Half-bridge Circuits", *Wide Bandgap Power Devices and Applications in Asia (WIPDA Asia) 2018 1st Workshop on*, pp. 130-134, 2018.
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3. Tianhua Zhu, Fang Zhuo, Fangzhou Zhao, Feng Wang, Tong Zhao, "Quantitative Model-Based False Turn-on Evaluation and Suppression for Cascode GaN Devices in Half-Bridge Applications", *Power Electronics IEEE Transactions on*, vol. 34, no. 10, pp. 10166-10179, 2019.
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Keywords

IEEE Keywords

Threshold voltage, MOSFET, Semiconductor device modeling, Electric potential, Semiconductor process modeling, Analytical models, Logic gates

INSPEC: Controlled Indexing

MOSFET, semiconductor device models

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BSIM6 MOSFET model, physical charge-based core, short channel effects, SPICE simulators, IBM technology, threshold voltage extraction technique, size 90 nm

Author Keywords

BSIM6, MOSFET, SPICE

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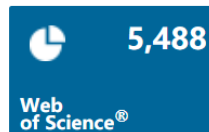
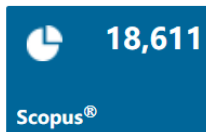
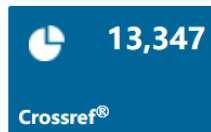
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Analytical Modeling and Experimental Validation of Threshold Voltage in BSIM6 MOSFET Model

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Chenming Hu

Department of Electrical Engineering and Computer
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瞭解作者詳情

Chenming Hu (F' 03) is the TSMC Distinguished Professor Emeritus of University of California Berkeley, Berkeley, CA, USA. He is a Former Chief Technology Officer of TSMC. He is a Board Director of SanDisk Inc., and of the non-profit Friends of Children with Special Needs. He is well known for his work on the 3-D transistor, FinFET, which can be scaled to single digit nanometers. He has developed widely used IC reliability models and led the research of BSIM—the first industry—standard SPICE model used by most IC companies to design CMOS products since 1996. He was a recipient of the IEEE Andrew Grove Award, the Solid State Circuits Award and Nishizawa Medal, the Kaufman Award of the EDA industry, the University Research Award of the U.S. Semiconductor Industry Association, and the UC Berkeley's Highest Honor for teaching—the Berkeley Distinguished Teaching Award.

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IEEE Transactions on Evolutionary Computation
Year: 2021 | Early Access Article | Publisher: IEEE

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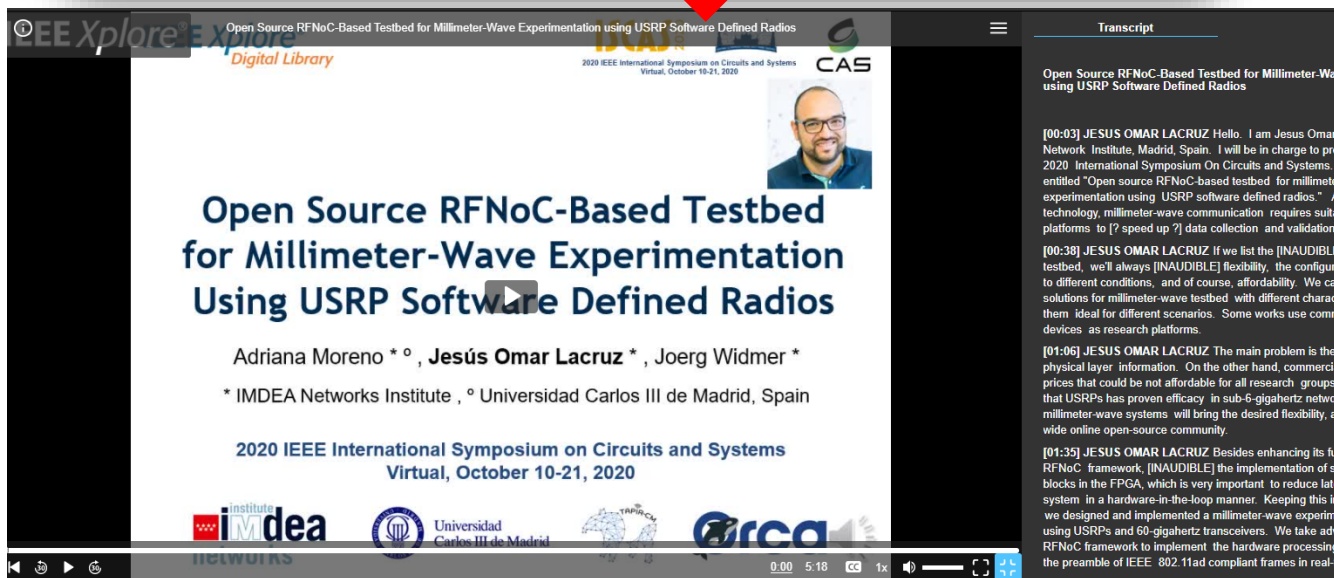
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會議視頻



Open Source RFNoC-Based Testbed for Millimeter-Wave Experimentation using USRP Software Defined Radios

Open Source RFNoC-Based Testbed for Millimeter-Wave Experimentation Using USRP Software Defined Radios

Adriana Moreno * °, Jesús Omar Lacruz *, Joerg Widmer *

* IMDEA Networks Institute , ° Universidad Carlos III de Madrid, Spain

2020 IEEE International Symposium on Circuits and Systems Virtual, October 10-21, 2020

Transcript

Open Source RFNoC-Based Testbed for Millimeter-Wave using USRP Software Defined Radios

[00:03] JESUS OMAR LACRUZ Hello. I am Jesus Omar Network Institute, Madrid, Spain. I will be in charge of the 2020 International Symposium On Circuits and Systems entitled "Open source RFNoC-based testbed for millimeter experimentation using USRP software defined radios." A technology, millimeter-wave communication requires suitable platforms to [?] speed up [?] data collection and validation

[00:38] JESUS OMAR LACRUZ If we list the [INAUDIBLE] testbed, we'll always [INAUDIBLE] flexibility, the configuration to different conditions, and of course, affordability. We can solutions for millimeter-wave testbed with different characteristics ideal for different scenarios. Some works use commercial devices as research platforms.

[01:06] JESUS OMAR LACRUZ The main problem is the physical layer information. On the other hand, commercial prices that could be not affordable for all research groups that USRPs has proven efficacy in sub-6-gigahertz networks millimeter-wave systems will bring the desired flexibility, a wide online open-source community.

[01:35] JESUS OMAR LACRUZ Besides enhancing its full RFNoC framework, [INAUDIBLE] the implementation of sub-blocks in the FPGA, which is very important to reduce latency system in a hardware-in-the-loop manner. Keeping this in mind, we designed and implemented a millimeter-wave experiment using USRPs and 60-gigahertz transceivers. We take the RFNoC framework to implement the hardware processing the preamble of IEEE 802.11ad compliant frames in real-

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Personalized Channel Recommendation Deep Learning From a Switch Sequence

Can Yang ; Sixuan Ren ; Yong Liu ; Houwei Cao ; Qihu Yuan ; Guoqiang Han

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Page s: 50824 - 50838

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CHANNELS SWITCH SEQUENCES OF 300 IPTV VIEWERS IN A MONTH

Citation Author(s): Sixuan Ren and Can Yang in South China University of Technology

Submitted by: Can Yang

Last updated: Thu, 11/08/2018 - 10:34

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This dataset includes the Channels Switch Sequences of 300 IPTV viewers in Guangzhou, P.R. China, in August, 2014. There are 4 columns in the file, which represent viewer ID, the current channel number, the next channel number, the date of the month, respectively. The first column, the ID code of a viewer, ranks in descent with the times the viewer watched tv channels. The more times a viewer watches tv channels, the bigger the ID is. In a day, the rows are time series and generated step by step as the real watching tv behavior.

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Mahmoud Saleh ; Yusef Esa ; Nwabueze Onuorah ; Ahmed A. Mohamed
2017 IEEE 6th International Conference on Renewable Energy Research and Applications (ICRERA)
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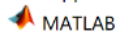
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H. Reisinger

Also published under: Hans Reisinger, Reisinger

Affiliation

Infineon Technologies AG
Neubiberg
Germany,85579

Publication Topics

MOSFET, semiconductor device reliability, semiconductor device models, silicon compounds, hot carriers, negative bias temperature instability, wide band gap

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Biography

Hans Reisinger received the Diploma degree in physics and the Ph.D. from the Technical University of Munich in 1979 and 1982, respectively. In 1986, he joined Infineon Technologies AG, where he was involved in thin dielectrics and MOSFET fabrication and characterization. He is currently a senior research engineer in the MOSFET reliability department, mainly working on the problems of threshold voltage instabilities of MOSFETs. (Based on document published on 26 October 2018).

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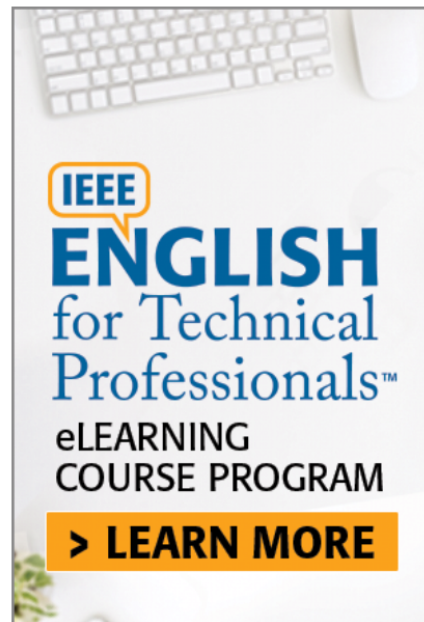
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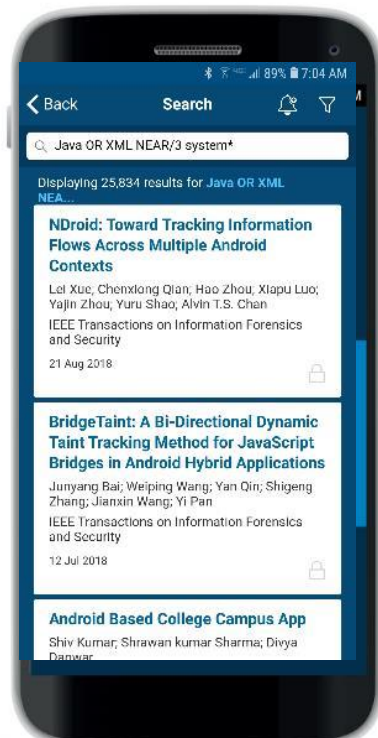
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