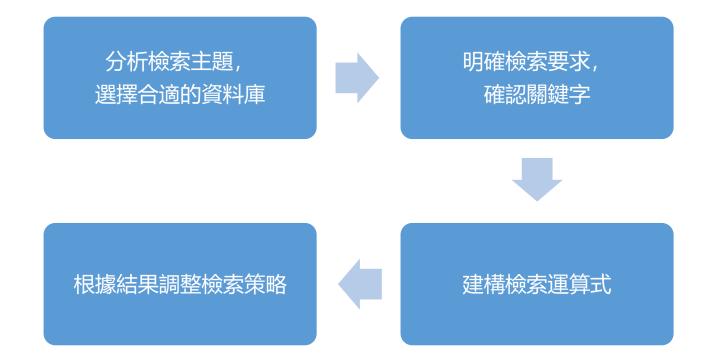


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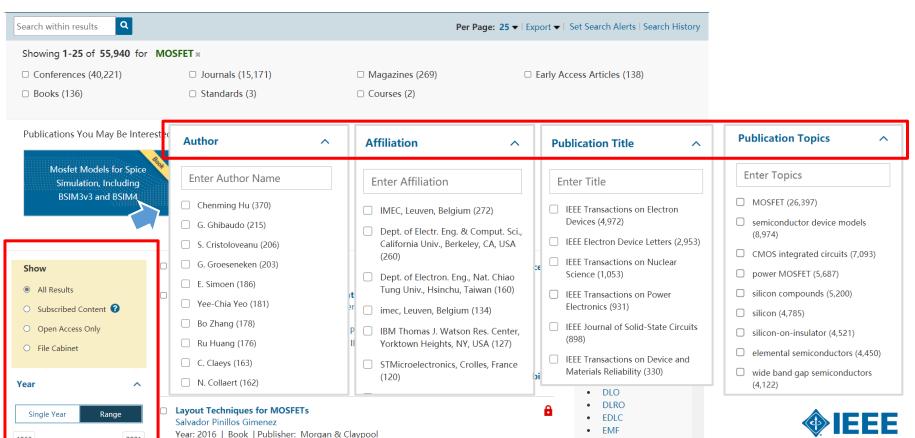


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Abstract

1963



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往期回顧——文章細節頁面





1. Tang: C. Burkhart

IEEE Transactions on Dielectrics and Electrical Insulation
Year: 2009 | Volume: 16, Issue: 4 | Journal Article | Publisher: IEEI
Cited by: Papers (8)

▶ Abstract

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Hybrid MOSFET/driver for ultra-fast switching

Publisher: IEEE





T. Tang: C. Burkhart All Authors

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Coreless printed circuit board (PCB) transformers for power MOSFET/IGBT gate drive circuits

IEEE Transactions on Power Electronics
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Document Sections

I. Introduction

II. Design

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Abstract:

The ultra-fast switching of power MOSFETs, in about 1 ns, is very challen the parasitic inductance that is intrinsic to commercial packages used fo

drivers. Parasitic gate and source inductance not only limit the voltage risinternal gate structure but can also cause the gate voltage to oscillate. This paper describes a hybrid approach that substantially reduces the parasitic inductance between the driver and MOSFET gate, as well as between the MOSFET source and its external connection. A flip-chip assembly is used to directly attach a die-form power MOSFET and driver on a PCB. The parasitic inductances are significantly reduced by eliminating bond wires and minimizing lead length. The experimental results demonstrate ultra-fast switching of the power MOSFET with excellent control of the gate-source voltage.

Published in: IEEE Transactions on Dielectrics and Electrical Insulation (Volume: 16, Issue: 4, August 2009)

Page(s): 967 - 970

INSPEC Accession Number: 10847239

9.5211841

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SECTION I.

Introduction

Power MOSFETs have great potential as switches for high speed high voltage applications like pulsed power. the theoretical carrier transit time from drain to source is on the order of 200 ps in any cell of the silicon die [1]. Although the power MOSFET is

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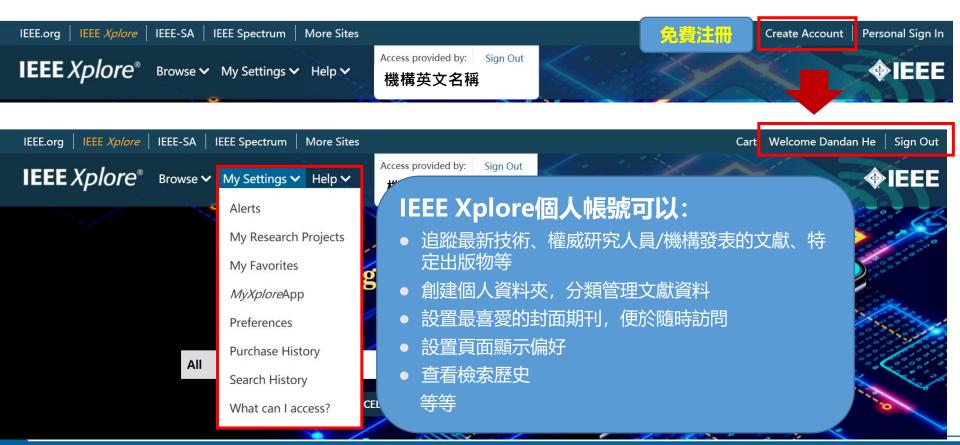
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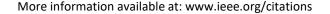
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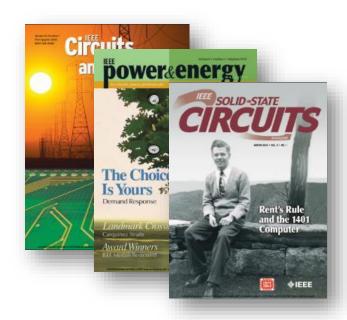




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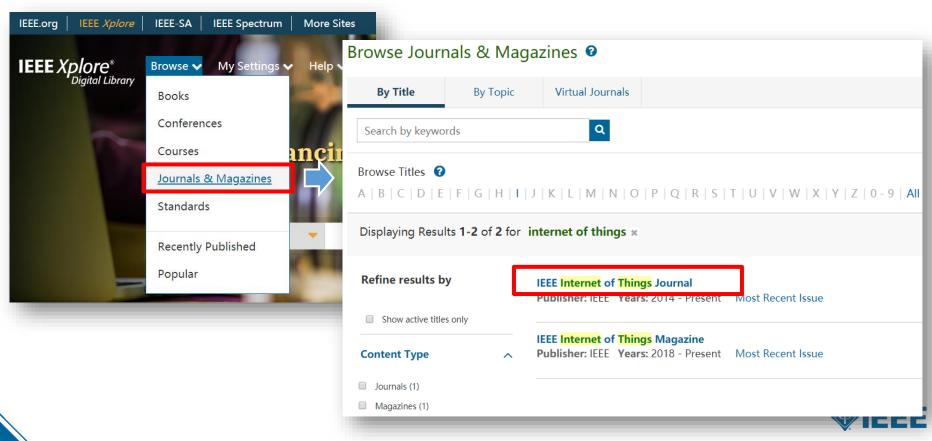
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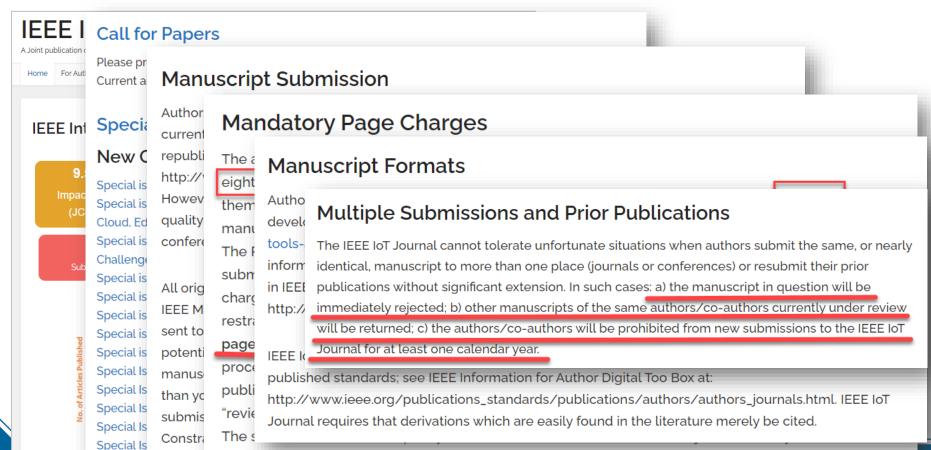
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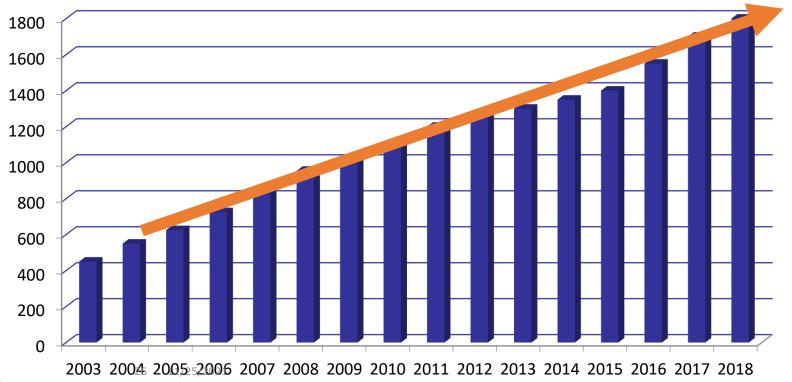
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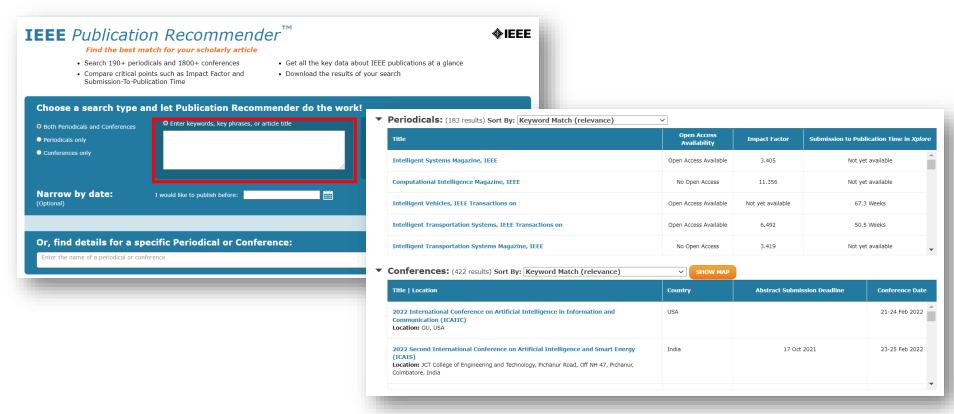


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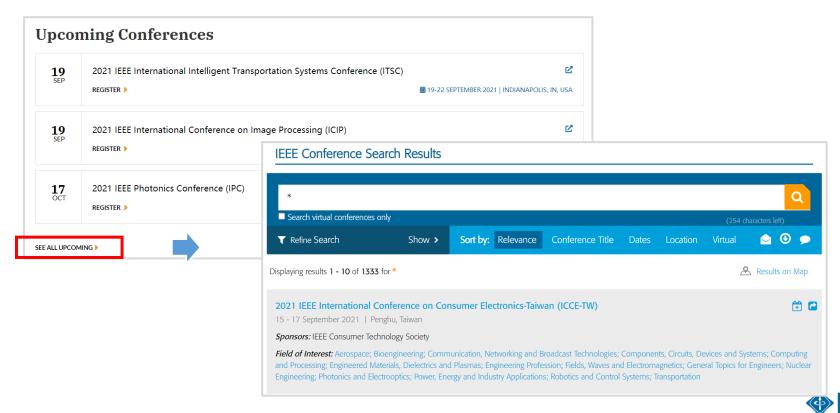
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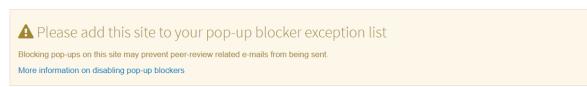
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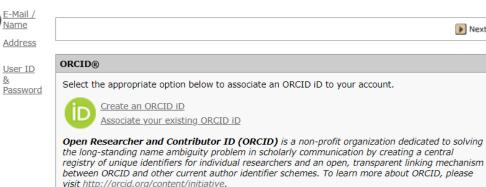
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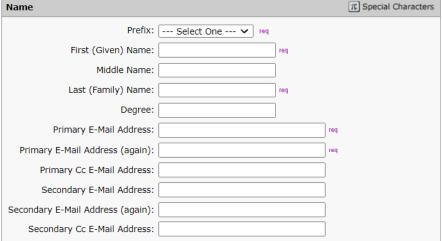


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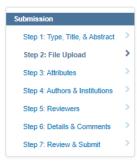
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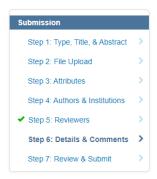
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Special Session Proposal Submission	August 15, 2021
Special Session Acceptance Notification	September 06, 2021
Tutorial Proposals Deadline	October 11, 2021
Full Paper Submission Deadline	October 06, 2021
Grand Challenge Proposal Submission	October 18, 2021
Grand Challenge Acceptance Notification	November 08, 2021
Tutorial Acceptance Notification	November 15, 2021
SPS Journal Papers and Letters Due	January 10, 2022
Paper Acceptance Notification	January 21, 2022
Final Paper Submission	February 16, 2022
Author Registration Deadline	February 16, 2022
Open Preview	April 22 - May 27, 2022
Show & Tell Proposals	March 07, 2022



Paper Submission

Papers must be formatted according to the instructions in the ICASSP 2022 Paper Kit.

Paper Kit (opens in a new window)

Please take care to read the **Conference Policies** from the IEEE Signal Processing Society!

If this is your first time to submit a paper to ICASSP 2022, please read the entire paper kit carefully to verify that your paper document is formatted correctly and that you have all the information you need before starting your paper submission. The paper kit contains detailed instructions on formatting your document and completing the submission process, as well as a description of how the review process works and how to prepare for your presentation at the conference if your paper is accepted.

If you are a returning author, you should review the paper kit, and note that the paper kit contains expanded information about the review process and preparing for a presentation at ICASSP 2022.

If you are submitting to a special session, you will receive separate instructions for submitting your paper. Please do not submit invited papers through the regular submission process unless indicated to do so.

ICASSP 2022 requires that each accepted paper be presented by one of the authors in-person at the conference site according to the schedule published. Any paper accepted into the technical program, but not presented will be withdrawn from the official proceedings archived on IEEE Xplore.

It is encouraged to include discussions on how your contributions are related to prior work in the field. It is important to put new work in context, to give credit to foundational work, and to provide details associated with the previous work that has appeared in the literature. It is not required that the discussion of relation to prior work be in a separate section, but the discussion must appear in the paper. An optional 5th page may be included, but that page may contain only references.

Equivalent Times for Paper S	ubmission Deadline
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Kolkata	17:30 Oct 07
Moscow	15:00 Oct 07
Riyadh	15:00 Oct 07
Cairo	14:00 Oct 07
Stockholm	14:00 Oct 07
London	13:00 Oct 07
New York	08:00 Oct 07
Dallas	07:00 Oct 07
Los Angeles	05:00 Oct 07
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Online Paper Submission

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See the Frequently Asked Questions section in the paper kit.

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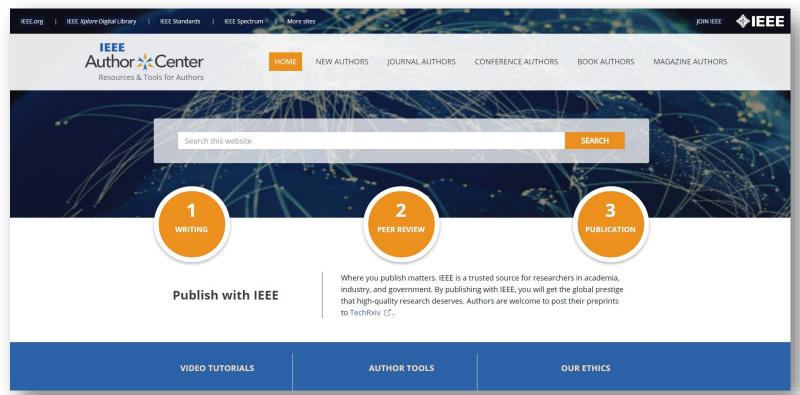
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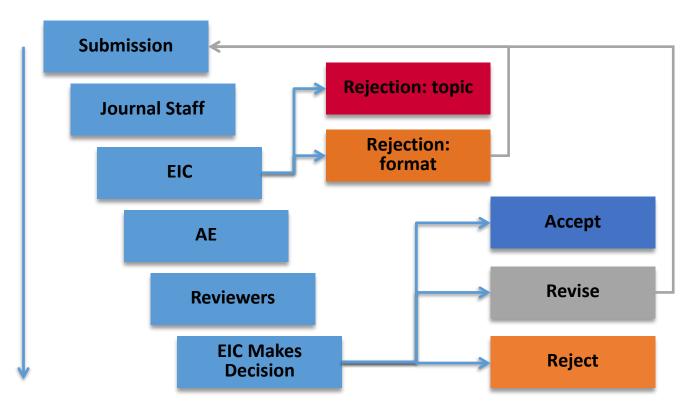
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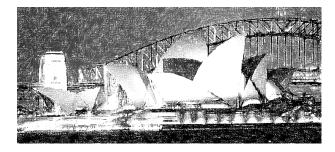


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